

Amendments to the Specification

On page 17, please delete the fourth full paragraph (lines 24-30) and replace with the following:

FIG. 8A is a top plan view of an electrode arrangement comprising interdigitated electrodes 810, ~~812~~ 820. The electrodes 810, ~~812~~ 820 are shown in isolation in FIG. 8A. A complete innerlayer panel 8100, including a capacitor 805 comprising the electrodes 810, ~~812~~ 820, is illustrated in FIG. 8I. A printed wiring board 8000 having the innerlayer panel 8100 incorporated therein is illustrated in FIG. 8J. The interdigitated electrode capacitor 805 is trimmable in order to adjust its capacitance.

On page 18, please delete the second full paragraph (lines 24-32) and replace with the following:

FIG. 8D is a top plan view of a next stage of manufacture. FIG. 8E is a section view taken on line 8E-8E in FIG. 8D. Referring to FIG. 8D, a photo-resist is applied to the first foil 32, and the foil 32 is imaged, etched and stripped. The electrodes 810, ~~812~~ 820, including the first and second elongated electrode portions 812, ~~814~~ 822, the serpentine trench 828, and the conductive portions 814, 824 result from the etching step. The second foil 34 may also be etched to form circuitry 8010. The circuitry 8010 illustrated in FIG. 8E can act as, for example, a power plane 8010 in the finished printed wiring board 8000 (illustrated in FIG. 8J).